

IEEE HOME | SEARCH IEEE | SHOP | WEB ACCOUNT | CONTACT IEEE


[Membership](#) [Publications/Services](#) [Standards](#) [Conferences](#) [Careers/Jobs](#)

 Welcome
 United States Patent and Trademark Office

[Help](#) [FAQ](#) [Terms](#) [IEEE Peer Review](#)
[Quick Links](#)

Welcome to IEEE Xplore®

- ☐ Home
- ☐ What Can I Access?
- ☐ Log-out

Tables of Contents

- ☐ Journals & Magazines
- ☐ Conference Proceedings
- ☐ Standards

Search

- ☐ By Author
- ☐ Basic
- ☐ Advanced

Member Services

- ☐ Join IEEE
- ☐ Establish IEEE Web Account
- ☐ Access the IEEE Member Digital Library

IEEE Enterprise

- ☐ Access the IEEE Enterprise File Cabinet

Print Format

 Your search matched **8** of **1062489** documents.

 A maximum of **500** results are displayed, **15** to a page, sorted by **Relevance Descending** order.

Refine This Search:

You may refine your search by editing the current search expression or enter a new one in the text box.

☐ Check to search within this result set

Results Key:

JNL = Journal or Magazine **CNF** = Conference **STD** = Standard
1 **A CMOS fully balanced four-terminal floating nullor**
Alzaher, H.; Ismail, M.;

Circuits and Systems I: Fundamental Theory and Applications, IEEE Transactions on [see also Circuits and Systems I: Regular Papers, IEEE Transactions on], Volume: 49, Issue: 4, April 2002

Pages:413 - 424

[\[Abstract\]](#) [\[PDF Full-Text \(402 KB\)\]](#) IEEE JNL
2 **CMRR enhancement techniques for current-mode instrumentation amplifiers**
Koli, K.; Halonen, K.A.I.;

Circuits and Systems I: Fundamental Theory and Applications, IEEE Transactions on [see also Circuits and Systems I: Regular Papers, IEEE Transactions on], Volume: 47, Issue: 5, May 2000

Pages:622 - 632

[\[Abstract\]](#) [\[PDF Full-Text \(200 KB\)\]](#) IEEE JNL
3 **Current-mode approach for wide-gain bandwidth product architecture**
Lee, C.-H.; Cornish, J.; McClellan, K.; Choma, J., Jr.;

Circuits and Systems II: Analog and Digital Signal Processing, IEEE Transactions on [see also Circuits and Systems II: Express Briefs, IEEE Transactions on], Volume: 45, Issue: 5, May 1998

Pages:626 - 631

[\[Abstract\]](#) [\[PDF Full-Text \(220 KB\)\]](#) IEEE JNL
4 **Design of current mode operational amplifier with differential-input differential-output**

Kuo-Hsing Cheng; Huei-Chi Wang;

Circuits and Systems, 1997. ISCAS '97., Proceedings of 1997 IEEE International Symposium on , Volume: 1 , 9-12 June 1997
Pages:153 - 156 vol.1

[\[Abstract\]](#) [\[PDF Full-Text \(320 KB\)\]](#) IEEE CNF

5 Current-mode approach for wide gain bandwidth product architecture

Chang-Hyeon Lee; Cornish, J.; Clellan, K.; Chama, J., Jr.;

Circuits and Systems, 1997. ISCAS '97., Proceedings of 1997 IEEE International Symposium on , Volume: 1 , 9-12 June 1997
Pages:229 - 232 vol.1

[\[Abstract\]](#) [\[PDF Full-Text \(360 KB\)\]](#) IEEE CNF

6 A new universal biquad using CDBAs

Salama, K.; Ozoguz, S.; Soliman, A.;

Circuits and Systems, 2001. MWSCAS 2001. Proceedings of the 44th IEEE 200 Midwest Symposium on , Volume: 2 , 14-17 Aug. 2001
Pages:850 - 853 vol.2

[\[Abstract\]](#) [\[PDF Full-Text \(209 KB\)\]](#) IEEE CNF

7 A high-speed CMOS current op amp for very low supply voltage operation

Bruun, E.;

Circuits and Systems, 1994. ISCAS '94., 1994 IEEE International Symposium on , Volume: 5 , 30 May-2 June 1994
Pages:509 - 512 vol.5

[\[Abstract\]](#) [\[PDF Full-Text \(304 KB\)\]](#) IEEE CNF

8 Current-mode analogue signal processing

Lidgey, J.; Toumazou, C.;

Bipolar Circuits and Technology Meeting, 1991., Proceedings of the 1991 , 9-1 Sept. 1991
Pages:224 - 232

[\[Abstract\]](#) [\[PDF Full-Text \(784 KB\)\]](#) IEEE CNF

[Home](#) | [Log-out](#) | [Journals](#) | [Conference Proceedings](#) | [Standards](#) | [Search by Author](#) | [Basic Search](#) | [Advanced Search](#) | [Join IEEE](#) | [Web Account](#) | [New this week](#) | [OPAC Linking Information](#) | [Your Feedback](#) | [Technical Support](#) | [Email Alerting](#) | [No Robots Please](#) | [Release Notes](#) | [IEEE Online Publications](#) | [Help](#) | [FAQ](#) | [Terms](#) | [Back to Top](#)

Copyright © 2004 IEEE — All rights reserved